### ATTESTATION OF CONFORMITY

Issued to: NINGBO AUSTA SOLAR TECH CO., LTD.

No.136 1-1, Haichuan Rd, Jiangbei District, Ningbo, China

For the product: Hybrid inverter

Trade name:

Type/Model: AU-1P4K3G-LE, AU-1P4.6K3G-LE, AU-1P5K3G-LE, AU-1P5.5K3G-LE, AU-

1P6K3G-LE

Ratings: See Annex

Manufactured by: NINGBO AUSTA SOLAR TECH CO., LTD.

No.136 1-1, Haichuan Rd, Jiangbei District, Ningbo, China

Requirements: Engineering Recommendation G99 Issue 1 – Amendment 9:2022

This Attestation is granted on account of an examination by DEKRA, the results of which are laid down in a confidential file no. 6169274.51

The examination has been carried out on one single specimen or several specimens of the product, submitted by the manufacturer. The Attestation does not include an assessment of the manufacturer's production. Conformity of his production with the specimen tested by DEKRA is not the responsibility of DEKRA.

DEKRA Testing and Certification (Shanghai) Ltd.

Kreny Lin

Certification Manager

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### Ratings of the test product:

	Specif	ications table			
	AU-	AU-	AU-	AU-	AU-
Model	1P4K3G-	1P4.6K3G-	1P5K3G-	1P5.5K3G-	1P6K3G-
	LE	LE	LE	LE	LE
Input					
PV Max (W)	6000	6900	7500	8300	9000
Vmax PV (V)	550	550	550	550	550
Isc PV (absolute Max.) (A)	26 x 2	26 x 2	26 x 2	26 x 2	26 x 2
Number of MPP trackers	2	2	2	2	2
Number of input strings	1/1	1/1	1/1	1/1	1/1
Max. PV input range (A)	18.5 x 2	18.5 x 2	18.5 x 2	18.5 x 2	18.5 x 2
MPPT Voltage Range (V)	80-500	80-500	80-500	80-500	80-500
Vdc range @ full power (V)	120-500	130-500	150-500	160-500	170-500
Battery (charge/discharge)		<u> </u>			
Battery type		Li-i	on/Lead-acid	etc.	
Battery Nominal Voltage (V)			51.2		
Battery Voltage Range (V)			40-60		
Max charge/discharge Current(A)	80	80	80	80	80
Max charge/discharge Power(W)	4000	4600	4800	4800	4800
AC Grid (input and output)					
Normal AC Voltage (VAC)	L/N/PE, 220Vac, 230Vac				
Frequency (Hz)			50 / 60		
Max. cont. Current (A)	19	22	23	26	28
Nominal Power (VA)	4000	4600	5000	5500	6000
Max. Power (W)	4000 4000	4600 4600	5000 5000	5500 5500	6000 6000
Max. apparent Power (VA)  Power factor(adjustable)	4000		1.0( -0.8~ +0.8		6000
AC Load output			110( 010 1010	,	
Normal Voltage (VAC)		I /N/P	E, 220Vac, 23	30Vac	
Frequency (Hz)			50 / 60	70 740	
Max. cont. Current (A)	19	22	23	26	28
Nominal Output Power (W)	4000	4600	5000	5500	6000
Max. output Power (W)	4000	4600	5000	5500	6000
Max. apparent Power (VA)	4000	4600	5000	5500	6000
Power factor	1000	1000	1.0	] 3000	3000
Others					
Ingress protection (IP)			IP65		
Protective class			Class I		
		0500 /		4500\	
Temperature (°C)			+60°C (Derati		
Inverter Isolation			olated (PV-AC	<u> </u>	
Overvoltage category		OVC III (	(AC Main), OV	C II (PV)	



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G99/1-9 A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules

Extract form test report number.: 6169274.51

### 1. Operating Range:

Р

Tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence. Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

The Interface Protection shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source. In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.

Pass or failure of the test should be indicated in the fields below (right hand side), for example with the statement "Pass", "No disconnection occurs", etc. Graphical evidence is preferred.

Note that the value of voltage stated in brackets assumes a **LV** connection. This should be adjusted for **HV** as required.

Model: AU-1P6K3G-	LE			Р
Test 1:				
Measured Voltage	Measured	Measured Power	Measured Power	Test Time
(V)	Frequency (Hz)	(W)	factor	(seconds)
195.66	47.00	5473.66	0.9995	20
Test 2:				
Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (Minutes)
195.68	47.50	5475.86	0.9994	90
Test 3:				
Measured Voltage	Measured	Measured Power	Measured Power	Test Time
(V)	Frequency (Hz)	(W)	factor	(Minutes)
253.22	51.50	6011.88	0.9993	90
Test 4:				
Measured Voltage	Measured	Measured Power	Measured Power	Test Time
(V)	Frequency (Hz)	(W)	factor	(Minutes)
253.20	52.00	6007.15	0.9992	15
Test 5:				
Measured Voltage (V)	Measured Frequency (Hz)	Measured Power (W)	Measured Power factor	Test Time (Minutes)
230.56	50.00	6015.25	0.9989	90
Test 6:				
Measured Voltage (V)	Ramp range	Test frequency ramp	Test Duration	Confirm no trip
196.5	47.0 Hz to 52.0 Hz	+1 Hzs <sup>-1</sup>	5.0s	No trip
254.5	52.0 Hz to 49.0 Hz	-1 Hzs <sup>-1</sup>	3.0s	No trip



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### 2. Power Quality - Harmonics:

P

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12, and measurements for the 2<sup>nd</sup> – 13<sup>th</sup> harmonics should be provided. The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment. For three phase **Power Generating Modules**, measurements for all phases should be provided.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC G5.

The rating of the **Power Generating Module** (per phase) should be provided below, and the Total Harmonic Distortion (THD) and Partial Weighted Harmonic Distortion (PWHD) should be provided at the bottom of this section.

Model: AU-1P4K3G-LE

Power Generating	Module	tested to BS	S EN 61000-3-1	2
------------------	--------	--------------	----------------	---

Power Generating Module rating per phase (rpp)	4	kVA	Harmonic % = Measured Value (A) x 23/rating per phase (kVA)
Single or three phase measurements (for single phase measurements, only complete L1 columns below)	Single phase PV	' inverter	

	,							
Harmonic	At 45-55%	of Register	Limit in BS EN 61000-					
панноніс	Measured	Value (MV)	in Amps	Measured	Value (MV)	in %	3-12	
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.028	-	-	0.322	-	-	8%	8%
3	0.083	-	-	0.955	-	-	21.6%	Not stated
4	0.007	-	-	0.081	-	-	4%	4%
5	0.032	-	-	0.368	-	-	10.7%	10.7%
6	0.007	-	-	0.081	-	-	2.67%	2.67%
7	0.018	-	-	0.207	-	-	7.2%	7.2%
8	0.006	-	-	0.069	-	-	2%	2%
9	0.015	-	-	0.173	-	-	3.8%	Not stated
10	0.006	-	-	0.069	-	-	1.6%	1.6%
11	0.008	-	-	0.092	-	-	3.1%	3.1%
12	0.006	-	-	0.069	-	-	1.33%	1.33%
13	0.007	-	-	0.081	-	-	2%	2%
THD	-	-	-	1.163	-	-	23%	13%
PWHD	-	-	-	1.400	-	-	23%	22%

THD = Total Harmonic Distortion



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Harmonic	At 100% of	Registered		Limit in BS EN 61000-				
Паппопіс	Measured	Value (MV)	in Amps	Measured	Value (MV)	in %	3-12	
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.220	-	-	1.265	-	-	8%	8%
3	0.760	-	-	4.370	-	-	21.6%	Not stated
4	0.054	-	-	0.311	-	-	4%	4%
5	0.575	-	-	3.306	-	-	10.7%	10.7%
6	0.046	-	-	0.265	-	-	2.67%	2.67%
7	0.313	-	-	1.800	-	-	7.2%	7.2%
8	0.047	-	-	0.305	-	-	2%	2%
9	0.197	-	-	1.133	-	-	3.8%	Not stated
10	0.046	-	-	0.265	-	-	1.6%	1.6%
11	0.112	-	-	0.644	-	-	3.1%	3.1%
12	0.048	-	-	0.276	-	-	1.33%	1.33%
13	0.112	-	-	0.644	-	-	2%	2%
THD	-	-	-	1.726	-	-	23%	13%
PWHD	-	-	-	1.290	-	-	23%	22%

THD = Total Harmonic Distortion



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Model: AU	-1P6K3G-LE							
Power Ge	nerating Mo	dule tested	to BS EN 6	1000-3-12				
Power Generating Module rating per phase (rpp)			6		kVA	Harmonic 9 Measured 9 23/rating per (kVA)	Value (A) x	
J	ree phase r		`					
single phase measurements, only complete			Single pha	se PV inver	ter			
L1 column	, ,						T	
Harmonic		of <b>Register</b>	-				Limit in BS	EN 61000-
	Measured	Value (MV)	in Amps	Measured Value (MV) in %			3-12	
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.046	-	-	0.353	-	-	8%	8%
3	0.119	-	-	0.912	-	-	21.6%	Not stated
4	0.007	-	-	0.054	-	-	4%	4%
5	0.042	-	-	0.322	-	-	10.7%	10.7%
6	0.008	-	-	0.061	-	-	2.67%	2.67%
7	0.025	-	-	0.192	-	-	7.2%	7.2%
8	0.007	-	-	0.054	-	-	2%	2%
9	0.024	-	-	0.184	-	-	3.8%	Not stated
10	0.007	-	-	0.054	-	-	1.6%	1.6%
11	0.010	-	-	0.077	-	-	3.1%	3.1%
12	0.007	-	-	0.054	-	-	1.33%	1.33%
13	0.009	-	-	0.069	-	-	2%	2%
THD	-	-	-	1.131	-	-	23%	13%
PWHD	-	-	-	1.322	-	-	23%	22%

THD = Total Harmonic Distortion



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Harmonic	At 100% of	Registered	Limit in BS EN 61000-					
Паппопіс	Measured	Value (MV)	in Amps	Measured	Value (MV)	in %	3-12	
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.076	-	-	0.291	-	-	8%	8%
3	0.202	-	-	0.774	-	-	21.6%	Not stated
4	0.012	-	-	0.046	-	-	4%	4%
5	0.100	-	-	0.383	-	-	10.7%	10.7%
6	0.011	-	-	0.042	-	-	2.67%	2.67%
7	0.062	-	-	0.238	-	-	7.2%	7.2%
8	0.012	-	-	0.046	-	-	2%	2%
9	0.051	-	-	0.196	-	-	3.8%	Not stated
10	0.011	-	-	0.042	-	-	1.6%	1.6%
11	0.027	-	-	0.104	-	-	3.1%	3.1%
12	0.011	-	-	0.042	-	-	1.33%	1.33%
13	0.018	-	-	0.069	-	-	2%	2%
THD	-	-	-	1.728	-	-	23%	13%
PWHD	-	-	-	1.153	-	-	23%	22%

THD = Total Harmonic Distortion



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### 3. Power Quality - Voltage fluctuations and Flicker:

Р

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC P28.

The standard test impedance is  $0.4~\Omega$  for a single phase **Power Generating Module** (and for a two phase unit in a three phase system) and  $0.24~\Omega$  for a three phase **Power Generating Module** (and for a two phase unit in a split phase system). Please ensure that both test and standard impedance are completed on this form. If the test impedance (or the measured impedance) is different to the standard impedance, it must be normalised to the standard impedance as follows (where the **Power Factor** of the generation output is 0.98 or above):

d max normalised value = (Standard impedance / Measured impedance) x Measured value.

Where the **Power Factor** of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the standard impedance.

The stopping test should be a trip from full load operation.

The duration of these tests needs to comply with the particular requirements set out in the testing notes for the technology under test.

The test date and location must be declared.

Test start date 2023-02-13 Test end date					2023-02-13			
Test location		No.99, Ho	ngye Road,	Suzhou Ind	ustrial Park,	Suzhou,	liangsu, P.R.	China
Model:		AU-1P6K3	G-LE					
		Starting		Stopping Runnir			ning	
	d(max)	d(c)	d(t)	d(max)	d(c)	d(t)	P <sub>st</sub>	P <sub>lt</sub> 2 hours
Measured Values at test impedance	0.56	0.27	0	1.43	0.27	0	0.22	0.19
Normalised to standard impedance	0.56	0.27	0	1.43	0.27	0	0.22	0.19
Normalised to required maximum impedance	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A
Limits set under BS EN 61000- 3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65



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Test Impedance	R	0.4	Ω	ΧI	0.25	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	XI	0.15 * 0.25 ^	Ω
Maximum Impedance	R	N/A #	Ω	ΧI	N/A #	Ω

<sup>\*</sup> Applies to three phase and split single phase Power Generating Modules.

### 4. Power quality – DC injection:

Ρ

The tests should be carried out on a single **Generating Unit**. Tests are to be carried out at three defined power levels ±5%. At 230 V a 50 kW three phase **Inverter** has a current output of 217 A so DC limit is 543 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.

The % DC injection ("as % of rated AC current" below) is calculated as follows:

% DC injection = Recorded DC value in Amps / Base current

where the base current is the **Registered Capacity** (W) / Vphase. The % DC injection should not be greater than 0.25%.

greater than 0.25%.			
Model: AU-1P4K3G-LE			
Single-phase			
Test power level	10%	55%	100%
Recorded DC injection value in Amps	0.030	0.030	0.029
as % of rated AC current	0.17%	0.17%	0.17%
Limit	0.25%	0.25%	0.25%
Model: AU-1P6K3G-LE			
Single-phase			
Test power level	10%	55%	100%
Recorded DC injection value in Amps	0.040	0.042	0.420
as % of rated AC current	0.15%	0.16%	0.16%
Limit	0.25%	0.25%	0.25%

<sup>^</sup> Applies to single phase **Power Generating Module** and **Power Generating Modules** using two phases on a three phase system. Delete as appropriate.



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5. Power Factor: P

The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity** and the measured **Power Factor** must be greater than 0.95 to pass. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2

Note that the value of voltage stated in brackets assumes a **LV** connection. This should be adjusted for **HV** as required.

Model: A	U-1P4K3G-LE	Ξ
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Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	0.9996	0.9996	0.9989
Power Factor Limit	>0.95	>0.95	>0.95
Model: AU-1P6K3G-LE	1		
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	0.9996	0.9996	0.9987
Power Factor Limit	>0.95	>0.95	>0.95

### 6. Protection – Frequency tests:

Ρ

These tests should be carried out in accordance with the Annex A.7.1.2.3. For trip tests, frequency and time delay should be stated. For "no trip tests", "no trip" can be stated.

### Model: AU-1P6K3G-LE

Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /	Confirm no
	rrequericy	Time delay	rrequericy	Time delay	time	trip
U/F stage 1	47.5 Hz	20 s	47.50 Hz	20.08s	47.7 Hz	No trip
On olago	17.0112	200	17.00112	20.000	30 s	140 thp
U/F stage 2	47.0 Hz	0.5 s	46.99 Hz	0.540s	47.2 Hz	No trip
Gr. diago 2	17.01.12	0.0 0	10.00 1.12	0.0403	19.5 s	
					46.8 Hz	No trip
					0.45 s	110 1115
O/F	52.0 Hz	0.5 s	52.00 Hz	0.548s	51.8 Hz	No trip
<i>O</i> 7.	02.01.2	0.0 0	02.001.12	0.0.100	120.0 s	110 11.19
					52.2 Hz	No trip
					0.45 s	

Note: For frequency trip tests the frequency required to trip is the setting  $\pm$  0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting  $\pm$  0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.



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### 7. Protection – Voltage tests:

P

These tests should be carried out in accordance with Annex A.7.1.2.2. For trip tests, voltage and time delay should be stated. For "no trip tests", "no trip" can be stated.

Note that the value of voltage stated below assumes a **LV** connection This should be adjusted for **HV** taking account of the VT ratio as required.

Model: AU-1P6K3G-LE

Function	Setting		Trip test		"No trip tests	"
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V	0.8 pu (184 V)	2.5 s	181.1V	2.536s	188 V 5.0 s	No trip
					180 V 2.45 s	No trip
O/V stage 1	1.14 pu (262.2 V)	1.0 s	265.1V	1.052s	258.2 V 5.0 s	No trip
O/V stage 2	1.19 pu (273.7 V)	0.5 s	276.6V	0.511s	269.7 V 0.95 s	No trip
					277.7 V 0.45 s	No trip

Note: For Voltage tests the Voltage required to trip is the setting  $\pm 3.45$  V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting  $\pm 4$  V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.



Limit is

0.5s

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0.203s

0.222s

#### Ρ 8. Protection - Loss of Mains test: These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4. For test condition A, EUT output = 100 % Pn, test condition B, EUT output = 50 % to 66 % Pn, and test condition C, EUT output = 25 % to 33 % $P_n$ . Model: AU-1P6K3G-LE The following sub set of tests should be recorded in the following table. Test Power 33% 66% 100% 33% 66% 100% -5% Q +5% Q and -5% Q -5% P +5% Q +5% P imbalance Test 22 Test 12 Test 31 Test 21 Test 5 Test 10 Trip time.

0.289s

0.160s

0.224s

0.280s

8. Loss of Mains Protection, Vector Shift Stability test:						
8. Loss of Mains Protection, Vector Shift Stability test:  P  This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the  Power Generating Module does not trip under positive / negative vector shift.						
Model: AU-1P6K3G-LE						
Start Frequency Change Confirm no trip						
Positive Vector Shift	49.5 Hz	+50 degrees	No trip			
Negative Vector Shift	50.5 Hz	- 50 degrees	No trip			
8. Loss of Mains Protect	ction, RoCoF Stability tes	t:		Р		
This test should be carrie	ed out in accordance with A	Annex A.7.1.2.6. Confirmation	n is required that the	ne		
Power Generating Mod	ule does not trip for the du	ration of the ramp up and ran	np down test.			
Model: AU-1P6K3G-LE						
Ramp range	Test frequency ramp:	Test Duration	ration Confirm no trip			
49.0 Hz to 51.0 Hz	+0.95 Hzs <sup>-1</sup>	2.1 s	No trip			
51.0 Hz to 49.0 Hz	-0.95 Hzs <sup>-1</sup>	2.1 s	No trip			



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#### 9. Limited Frequency Sensitive Mode - Over frequency test:

F

The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%.

This test should be carried out in accordance with A.7.1.3, which also contains the measurement tolerances.

**Active Power** response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4

Ν

Model: AU-1P6K3G-LE

Alternatively, simulation results should be noted below:

Test sequence at	Measured	Frequency	Calculated	Primary	Active
Registered	Active	(Hz)	droop (%)	Power	Power
Capacity >80%	Power			Source	Gradient
	Output (W)				
Step a) 50.00 Hz ±0.01 Hz	6002.40	50.00	-		-
Step b) 50.45 Hz ±0.05 Hz	5938.28	50.45	9.37	_	-
Step c) 50.70 Hz ±0.10 Hz	5629.81	50.70	9.66	Photovoltaic	-
Step d) 51.15 Hz ±0.05 Hz	5094.09	51.15	9.92	array	-
Step e) 50.70 Hz ±0.10 Hz	5615.82	50.70	9.30	simulator	-
Step f) 50.45 Hz ±0.05 Hz	5938.42	50.45	9.42		-
Step g) 50.00 Hz ±0.01 Hz	6001.91	50.00	-		-
Test sequence at	Measured	Frequency	Calculated	Primary	Active
Registered Capacity 40-	Active	(Hz)	droop (%)	Power	Power
60%	Power			Source	Gradient
	Output (W)				
Step a) 50.00 Hz ±0.01 Hz	3002.87	50.00	-		-
Step b) 50.45 Hz ±0.05 Hz	2939.89	50.45	9.52	]	-
Step c) 50.70 Hz ±0.10 Hz	2618.29	50.70	9.36	Photovoltaic	-
Step d) 51.15 Hz ±0.05 Hz	2070.63	51.15	9.65	array	-
Step e) 50.70 Hz ±0.10 Hz	2620.65	50.70	9.42	simulator	-
		50.45	0.54		
Step f) 50.45 Hz ±0.05 Hz	2942.07	50.45	9.51		-

The frequency at each step should be maintained for at least one minute and the Active Power reduction in the form of a gradient determined and assessed for compliance with paragraph 11.2.3. The Droop should be determined from the measurements between 50.4 Hz and 51.15 Hz. The allowed tolerance for the frequency measurement shall be  $\pm$  0.05 Hz. The allowed tolerance for Active Power output measurement shall be  $\pm$ 10% of the required change in Active Power.

The resulting overall tolerance range for a nominal 10% Droop is +2.8% and -1.5%, ie a Droop less than 12.8% and greater than 8.5%.



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9-2. Power output with falling frequency test (For PV Inverter):						
Tests should prove that the <b>Power Generating Module</b> does not reduce output power as the frequency						
falls. These tests should	d be carried out in acco	ordance with 11.2.3.1,	12.2.3.1, 13.2.3.1.			
Model: AU-1P6K3G-LE						
Test sequence	Measured Active	Acceptable <b>Active</b>	Frequency	Primary power		
	Power Output (W)	Power	(Hz)	source		
49.5 Hz for 5 minutes	6004.11	100% Registered	49.50	Photovoltaic		
49.3 112 101 3 1111111111111111111111111111	0004.11	Capacity	49.50	array simulator		
49.0 Hz for 5 minutes	6003.79	99% Registered	49.00	Photovoltaic		
43.0 112 101 3 111111111111111	0003.79	Capacity	49.00	array simulator		
48.0 Hz for 5 minutes	6004.05	97% Registered	48.00	Photovoltaic		
40.0 112 101 3 111111111111111	0004.03	Capacity	40.00	array simulator		
47.6 Hz for 5 minutes	6004.19	96.2% Registered	47.60	Photovoltaic		
47.0112 101 3 Hilliates	0004.19	Capacity	47.00	array simulator		
47.1 Hz for 20 s	6004.52	95% Registered	47.10	Photovoltaic		
47.1112101203	0004.32	Capacity	77.10	array simulator		

9-3. Power output with falling frequency test (For Electricity Storage Device)						
This test should be carried out in accordance with clause 11.2.3.3, 12.2.3.3, 13.2.3.2 and A.7.1.7						
Model: AU-1P6K3G-	·LE					
Test 1: 50 Hz to 49.0	Hz, from <b>100%</b> Prated	-import				
Test sequence	Measured Active	Steady frequency	Calculated droop	Primary power		
(Hz)	Power Output (W)	(Hz)	(%)	source		
50.0	-4724.85	50.00	-	AC grid / Storage		
				Battery		
49.5	-4794.79	49.50	-	AC grid / Storage		
				Battery		
49.2	-2024.88	49.20	1.04%	AC grid / Storage		
			110 170	Battery		
49.0	-117.59	49.00	1.03%	AC grid / Storage		
49.0	-117.59	43.00	1.0570	Battery		
Test 2: 50 Hz to 48.8	B Hz, from <b>100%</b> Prated	-import				
Test sequence	Measured Active	Steady frequency	Calculated droop	Primary power		
(Hz)	Power Output (W)	(Hz)	(%)	source		
50.0	-4724.84	50.00		AC grid / Storage		
50.0	-4/24.04	50.00	-	Battery		
40.5	4700.07	40.50		AC grid / Storage		
49.5	-4792.67	49.50	-	Battery		
49.2	-2037.38	49.20	1.05%	AC grid / Storage		
43.4	-2037.30	43.20	1.00%	Battery		
49.0	-122.58	49.00	1.03%	AC grid / Storage		
49.0	-122.30	49.00	1.03%	Battery		



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48.9	885.15	48.90	1.01%	AC grid / Storage Battery
48.8	1885.80	48.80	1.01%	AC grid / Storage Battery
Test 3: 50 Hz to 49.0	Hz, from 40% Prated-in	mport		
Test sequence	Measured Active	Steady frequency	Calculated droop	Primary power
(Hz)	Power Output (W)	(Hz)	(%)	source
50.0	-1935.69	50.00	-	AC grid / Storage Battery
49.5	-1920.26	49.50	-	AC grid / Storage Battery
49.2	856.00	49.20	1.04%	AC grid / Storage Battery
49.0	2927.63	49.00	0.99%	AC grid / Storage Battery

Test 4: 50 Hz to 48.8 Hz, from 40% P <sub>rated-import</sub>					
Test sequence	Measured Active	Steady frequency	Calculated droop	Primary power	
(Hz)	Power Output (W)	(Hz)	(%)	source	
50.0	-1795.69	50.00	_	AC grid / Storage	
30.0	-1795.09	30.00	_	Battery	
49.5	-1895.96	49.50	_	AC grid / Storage	
49.5	-1095.90	49.50	_	Battery	
49.2	848.58	49.20	1.05%	AC grid / Storage	
49.2	040.30	49.20	1.0376	Battery	
49.0	2896.09	49.00	1.00%	AC grid / Storage	
49.0	2090.09	49.00	1.0076	Battery	
48.9	3893.72	48.90	0.99%	AC grid / Storage	
40.9	3093.72	40.90	0.9976	Battery	
48.8	4704.70	48.80	1.00%	AC grid / Storage	
40.0	4794.79	40.00	1.00 /0	Battery	

### NOTE:

This paragraph provides a method for demonstrating compliance with the optional performance characteristic as discussed in the foreword. The tests shall be carried out to demonstrate how the Power Park Module Active Power when acting as a load (ie replenishing its energy store) responds to changes in system frequency.

In general four tests are proposed, one set of two at rated import capacity, and one set of two at 40% of rated import capacity.

In both cases the test is to reduce frequency from 50 Hz at rate of 2 Hz/s. In the first case the lower frequency reached will be 49.0 Hz and the second case the lower frequency will be 48.8 Hz. In all cases the response shall meet the requirements of 11.2.3.3.



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#### 10. Protection - Re-connection timer

Ρ

Model: AU-1P6K3G-LE

Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1. Both the time delay setting and the measured delay should be provided in this form; both should be greater than 20 s to pass. Confirmation should be provided that the **Power Generating Module** does not reconnect at the voltage and frequency settings below; a statement of "no reconnection" can be made.

Time delay	Measured	Checks on no red	connection when v	oltage or frequenc	y is brought to	
setting	delay	just outside stage 1 limits of Table 10.1.				
30 s	30.8 s	At 1.16 pu (266.2 V <b>LV</b> )	At 0.78 pu (180.0 V <b>LV</b> )	At 47.4 Hz	At 52.1 Hz	
Confirmation tha	t the <b>Power</b>	No	No	No	No	
Generating Mod	dule does not					
re-connect.		Reconnection	Reconnection	Reconnection	Reconnection	
Recover to norm	Recover to normal operation					
range after confirmation of no		Yes	Yes	Yes	Yes	
connection						
Confirmation that the Power Generating Module shall		Reconnection	Reconnection	Reconnection	Reconnection	
					1 10001001	
reconnect		after 30.8 s	after 30.8 s	after 30.8 s	after 30.8 s	

# These tests shall be carried out in accordance with EREC G99 Annex A.7.1.5. Please complete each

entry, even if the contribution to the fault level is zero.

Model: AU-1P6K3G-LE

For Inverter output

Time after fault	Volts	Amps
20ms	177.9 V	18.82 A
100ms	1.076 V	15.99 A
250ms	0	0
500ms	0	0
Time to trip	83 ms	In seconds



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12. Self-Monitoring solid state switching: No specified test requirements. Refer to Anne	ex A.7.1.6.
It has been verified that in the event of the solid state switching device failing to disconnect the <b>Power Park Module</b> , the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.	N/A
13. Wiring functional tests: If required by para 15.2.1.	
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	N/A
14. Logic interface (input port).	
Confirm that an input port is provided and can be used to shut down the module.	Yes
Provide high level description of logic interface, e.g. details in 11.1.3.1 such as AC or DC signal (the additional comments box below can be used)	Yes
15. Cyber security	
	Yes,
Confirm that the Power Generating Module has been designed to comply with cyber	Manufacturer's
security requirements, as detailed in 9.1.7.	declaration
	provided
A LPC L	

#### Additional comments.

To short or open pin1 and pin5 of logic interface port (Com 1) to control the inverter to normal or shutdown active power of output. A logic interface is provided that can be operated by an external switch or contactor. Users can install by themselves. Users install the switch connected to pin1 and pin5 of Com1 and just need control the switch signal causing the switch to open or short. When the switch is closed, the inverter will operate normally. When the switch is opened, the inverter will cease to export active power within 5 seconds. The signal from the inverter that is being switched is DC (maximum value 3.3V)

End